

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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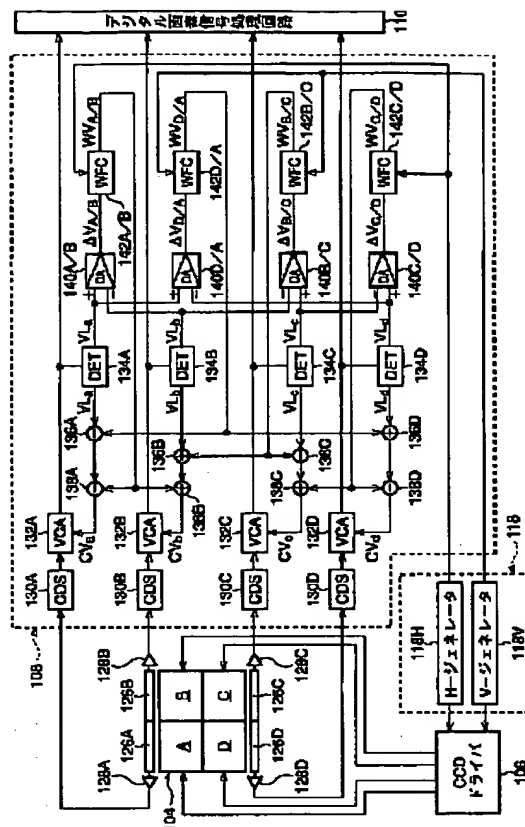
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APPLICANT : ASAHI OPTICAL CO LTD;

INVENTOR : SATO KOICHI;

INT.CL. : H04N 5/335 H04N 5/232

TITLE : PIXEL SIGNAL PROCESSING UNIT



**ABSTRACT :** PROBLEM TO BE SOLVED: To prevent borders of block areas adjacent to each other in an image- sensing region of a CCD image-pickup sensor or the like at reproduction of an image as a luminance difference.

**SOLUTION:** This pixel signal processing unit processes a pixel signal from 1st and 2nd block regions of an image-sensing region of a solid-state image- pickup sensor 104. A pixel signal read means 106 reads the pixel signal one line each from the 1st and 2nd block regions simultaneously and correspondingly. In this case, the pixel signal at the head among pixel signals included in each line is apart most remote from the borders of the 1st and 2nd block regions, and the pixel signal at the final end among the pixel signal included in each line is closest to the border, a signal level adjustment means (123A or the like) gradually decreases the level difference between the level of the pixel signal from the 1st block region and the level of the pixel signal from the 2nd block region toward the border, so as to substantially match the levels of the pixel signals at the final end lines.

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